



501.40724X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): M. Sahara, et al.

Application No.: 09/974,814

Filing Date: October 12, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND THE  
PROCESS OF THE SAME

Art Unit: 2814

Examiner: T. Quach

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 20, 2003

Sir:

In response to the Office Action mailed June 20, 2003, please amend the  
above-identified application as listed below, and as set forth on the following pages:

Amendments to the Specification;

Amendments to the Claims; and

Remarks are included following the amendments.

RECEIVED  
OCT 24 2003  
TECHNOLOGY CENTER 2800

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02 FC:1202 576.00 DP  
03 FC:1201 86.00 DP